



S/N10/761,995

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jih-Kwon Peir et al.

Examiner: Zhuo Li

Serial No.: 10/761,995

Group Art Unit: 2189

Filed: January 21, 2004

Docket No: 884.220US2

Title: CACHE LINE PRE-LOAD AND PRE-OWN BASED ON CACHE COHERENCE
SPECULATION

Assignee: Intel Corporation

Customer Number: 21186

PRELIMINARY AMENDMENT

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Prior to taking up the above-identified patent application for examination, please amend
the application as follows: